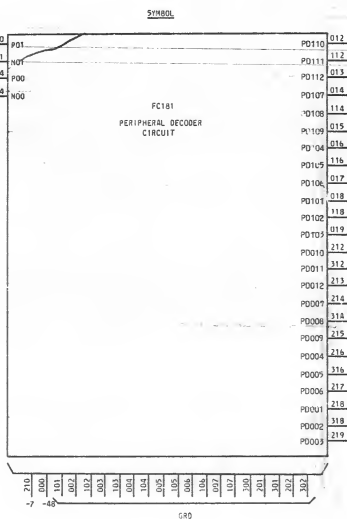


SHEET INDEX		
CONTENTS	SHEET NO.	SHEET ISSUE
SHEET INDEX SUPPORTING INFORMATION CURRENT DRAIN USED ON NOTES SYMBOL RECORD OF CHANGES	1	2
CIRCUIT SCHEMATIC	2	2
CIRCUIT SCHEMATIC	3	2
CIRCUIT SCHEMATIC COMPONENT LIST	4	2

SYMBOL
PERIPHERAL DECODER
ELEMENT IDENT
A

TERM.	MOD	FUNCT.	TERM.	LOC.
N00		I	304	
N01		I	001	
P00		I	204	
P01		I	100	
PD010		B	212	3A8
PD011		B	312	3A8
PD012		B	213	3A8
PD007		B	214	3C8
PD008		B	314	3C8
PD009		B	215	308
PD004		B	216	308
PD005		B	316	3E8
PD006		B	217	3E8
PD001		B	218	3F8
PD002		B	318	3F8
PD003		B	219	3G8
PD110		B	012	238
PD111		B	112	238
PD112		B	013	298
PD107		B	014	2C8
PD108		B	114	208
PD109		B	218	208
PD104		B	016	208
PD105		B	116	2E8
PD106		B	017	2F8
PD101		P	018	2F8
PD102		L	20F	GR0
PD103		B	019	258
-48		P	000	4E6
-7		P	001	4E5
GR0		G	002	4E6
GR0		G	003	4E6
GR0		G	004	4E6
GR0		G	005	4E7
GR0		G	006	4E7
GR0		G	007	4E7



SYMBOL
PERIPHERAL DECODER
ELEMENT IDENT
A

TERM.	MOD	FUNCT.	TERM.	LOC.
GR0		G	101	4E6
GR0		G	102	4E6
GR0		G	103	4E6
GR0		G	104	4C7
GR0		G	105	4E7
GR0		G	106	4E7
GR0		G	107	4E7
GR0		G	201	4E8
GR0		G	202	4E8
GR0		G	300	4E8
GR0		G	301	4E8
GR0		G	302	4E8

RECORD OF CHANGES			
DWG	REV	DATE	BY
155	1	10-29-55	WRE

NOTES:

1. UNLESS OTHERWISE SPECIFIED:
- RESISTANCE VALUES ARE IN OHMS.
- CAPACITANCE VALUES ARE IN MICROFARADS
VALUES PRECEDED BY THE SYMBOL "C" (PLUS) OR "-" (MINUS) ARE IN VOLTS.

2. POWER AND GROUND TERMINALS FOR INTEGRATED CIRCUITS:

VCC	TERM.	BIT
MODE	TERM.	TERM.
163E	5,7,22,32	6,16

3. BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
-7	210
-48	000
GR0	002,003,004, 005,006,007, 101,102,103, 104,105,106, 107,201,202, 300,301,302,

4. GROUND TERMINALS FOR THE CIRCUIT MODULE ARE:

ON	GR0
MODE	TERM.
178A	10,12,19,23,27

5. THE TERMINAL NUMBER ARRANGEMENT OF THE 918, 919 AND 946 TYPE TRANSISTOR IS:



6. GROUND RETURN

SYSTEM	DESIGN
USED ON	CONTROL
NO. 3 ESS	IN

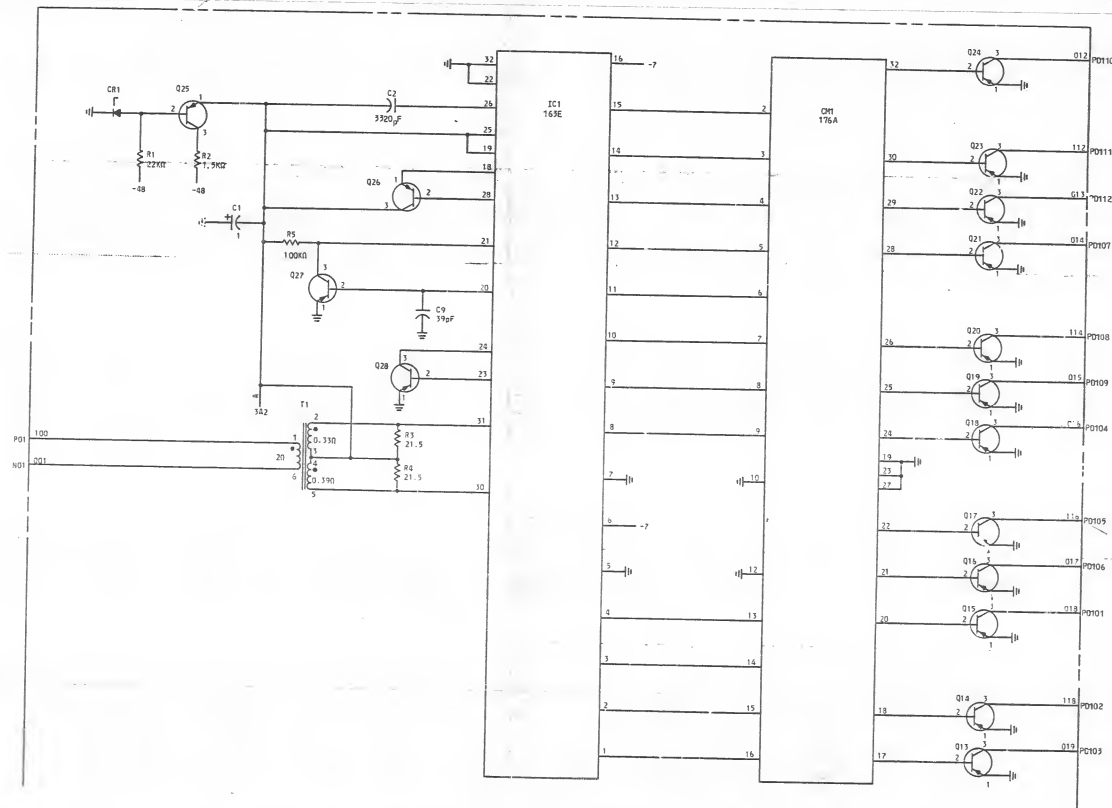
7. CLOSEST RECOMMENDED HORIZONTAL MOUNTING CENTERS IS 0.750 INCH.

CURRENT DRAIN: ON - 85V - 3mA MAX

SHEET INDEX NOTES		SUPPORTING INFORMATION		NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.		
1. FOR SYMBOL REVISIONS, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.		CATEGORY	NUMBER	FC181 CIRCUIT PACK PERIPHERAL DECODER CIRCUIT	1711	
2. FOR CONSECUTIVE REVISIONS, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE HIGHEST ISSUE NUMBER AFFECTING THAT SHEET.		CONNECTOR ON FRAME	9424, 9425 OR 9426			
3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.		CIRCUIT PACK INFORMATION DRAWING				
		SERIES FOR LATEST CLASS "A" CHANGE				
		ACCEPTABLE SERIES	1-3/3, 3/3, 3/3	BELL LABORATORIES - CPS - FC181		
				4 SHEETS		



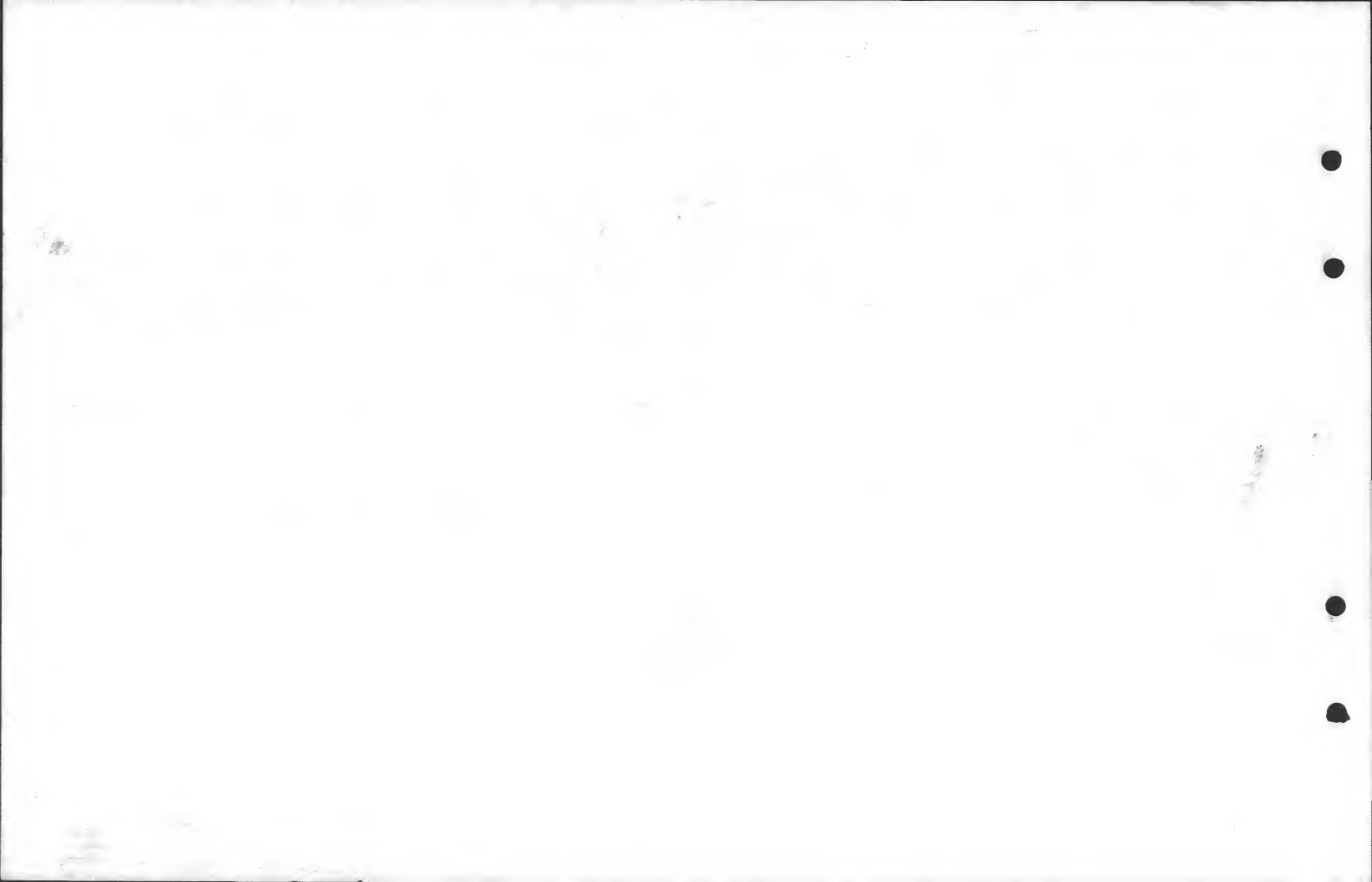
PERIPHERAL DECODER CIRCUIT



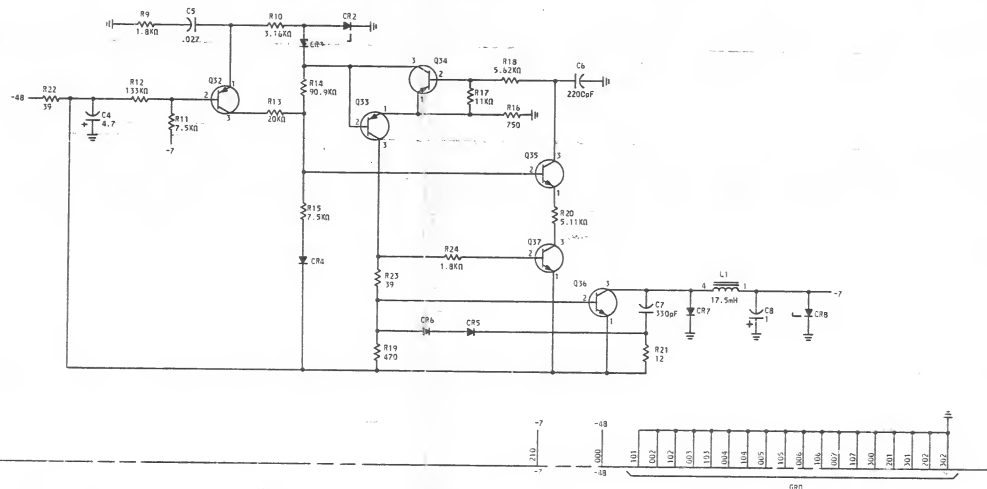
200
207
304

163E





PERIPHERAL DECODER CIRCUIT.



COMPONENT LIST

CAPACITOR

DESIG	CODE
C1	6001
[2] C2, C3	KS-16742, L27, 5320PF
C4	6014
C5	KS-19774, L05, .022
CA	KS-20900, L02, 2200PF
C7	KS-19774, L02, 330PF
CB	6028
[2] C9, C10	KS-19774, L7, 33PF

CIRCUIT MODULE

DESIG	CODE
[2] CH1, CH2	FIC 176A

INDUCTOR

DESIG	CODE
CR1	4590
CR2	4598
CR3	458A
CR4	4650
[3] CR5-CR7	458A
CR8	ROBE
INDUCTOR	CODE
LI	1628AA, 17.5MH

INTEGRATED CIRCUIT

DESIG	CODE
[2] IC1, IC2	167C

RESISTOR

DESIG	CODE
R1	KS-16645, L2, 22K
R2	KS-17750, L2, 1.5K
[2] R3, R4	KS-20616, L18, 21.5
R5	KS-16645, L2, 100K
[2] R6, R7	KS-20616, L18, 21.5
R8	KS-16645, L2, 100K
R9	KS-16645, L2, 1.8K
R10	KS-20616, L14, 5.74K
R11	KS-20616, L18, 7.5K
R12	KS-20616, L14, 137K
R13	KS-16645, L1, 20K
R14	KS-20616, L18, 90.9K
R15	KS-20616, L18, 7.5K
R16	KS-20616, L18, 750
R17	KS-20616, L18, 11K
R18	KS-20616, L18, 5.624K
R19	KS-16645, L2, 470
R20	KS-20616, L18, 5.1K
R21	KS-16645, L2, 12
[2] R22, R23	KS-16645, L2, 29
R24	KS-16645, L2, 1.8K

TRANSFORMER

DESIG	CODE
[2] T1, T2	259A3

TRANSISTOR

DESIG	CODE
[2] Q1-Q25	518
Q26-Q31	510
Q32-Q34	518
[3] Q35-Q37	66AE

PC181 CIRCUIT PACK

BELL TELEPHONE LABORATORIES
INCORPORATED

CPS-FC181

SHEET 4

2A1

